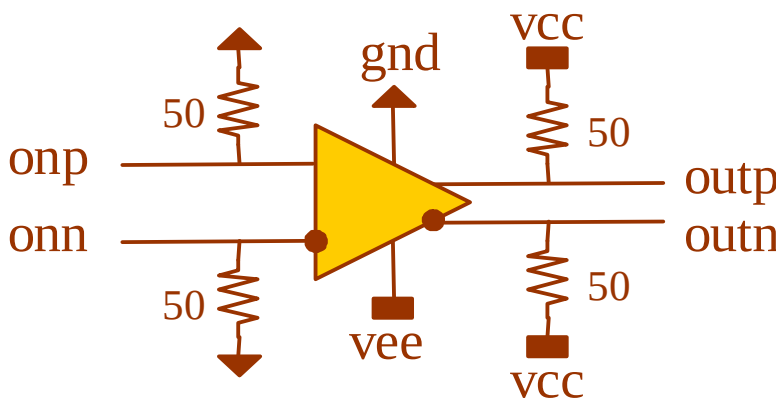
ADSANTEC
Ultra High-Speed Mixed Signal ASICs
Advanced Sience And Novel Technologies Company, Inc.
27 Via Porto Grande, Rancho Palos Verdes, CA 90275
Phone: 310-377-6029 / 310-803-9284 | Fax: 310-377-9940
Website: www.adsantec.com

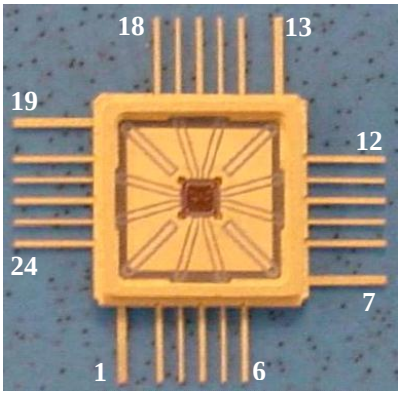
ASNT3111-KMC
32*Gbps* Single-Channel CML Level Shifter

- High-speed broadband CML data Level Shifter for signal distribution.
- Fully differential input and output buffers with on-chip 50*Ohm* termination.
- Exhibits low jitter and limited temperature variation over industrial temperature range.
- Fabricated in SiGe for high performance, yield, and reliability.
- Power consumption: 260 *mW*.
- Custom leaded 24-pin metal ceramic package.

DESCRIPTION



Functional Block Diagram



Package View

The ASNT3111-KMC SiGe IC provides a signal voltage shift from CML levels in devices operating with negative power supply to PCML levels in devices with positive power supply.

The part's I/Os support the CML logic interface with on chip 50 *Ohm* termination to ground ("gnd") for input pins, on chip 50 *Ohm* termination to positive "vcc" for outputs, and can be used differentially, AC/DC coupled, or single-ended.

The chip operates from two independent 3.3V power supplies: one negative ("vee") and one positive ("vcc").

The diagrams below demonstrate the chip's simulated performance.

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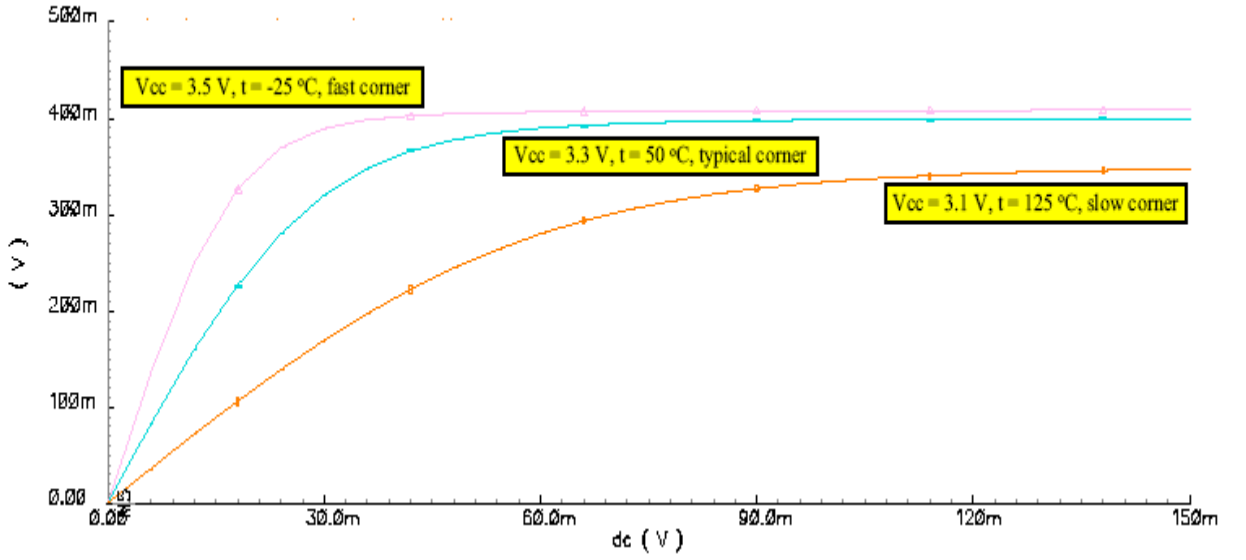
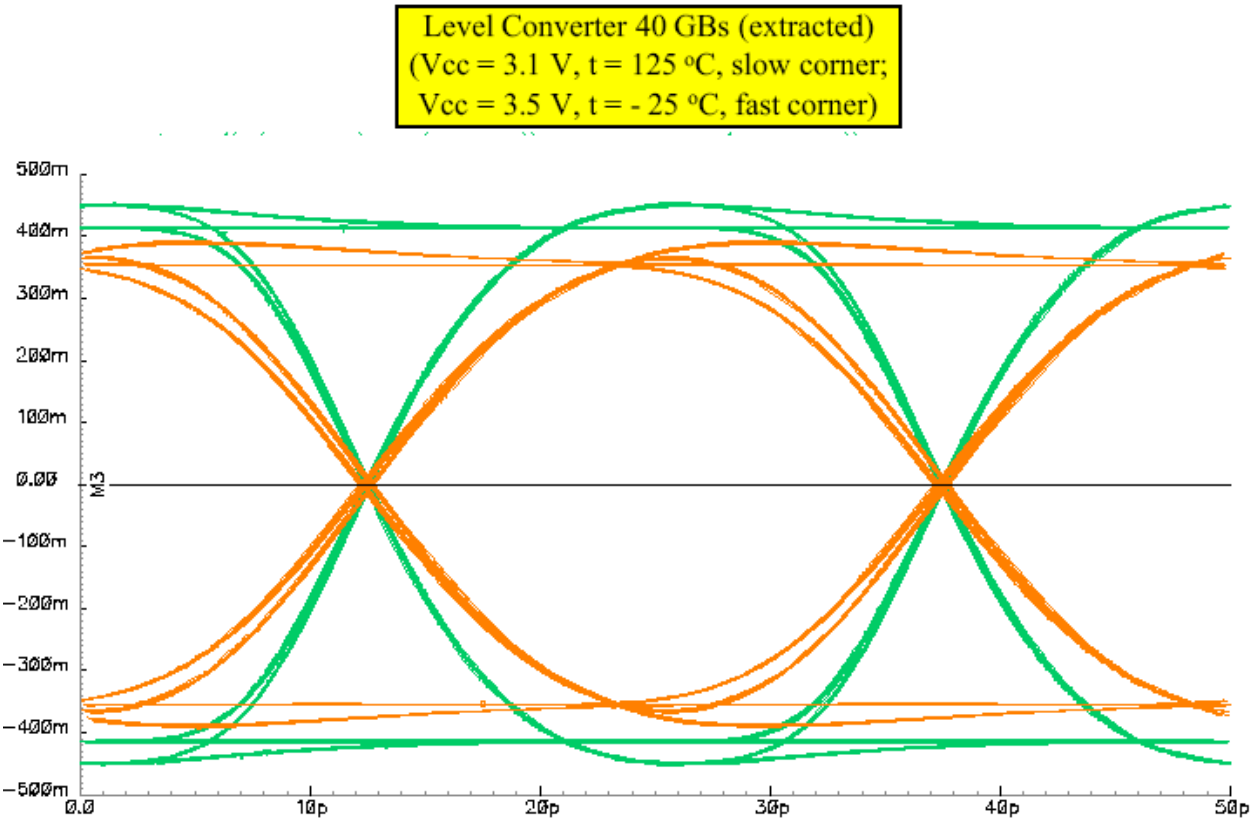


DC Transfer Function (Simulation).



40Gbps Output Eye Diagram (Simulation).

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TERMINAL FUNCTIONS

TERMINAL		TYPE	DESCRIPTION
NAME	(NO.)		
vcc	5, 15	PS	Power Supply: +3.3*V*
gnd	2, 3, 4, 6, 8, 10, 12, 14 16, 17, 18, 20, 22, 24	PS	Power Supply: 0*V*
vee	1, 7, 13, 19	PS	Power Supply: -3.3*V*
inp	21	Input	Differential CML high-speed inputs
inn	23	Input	
outp	11	Output	Differential CML high-speed outputs
outn	9	Output	

ELECTRICAL CHARACTERISTICS

PARAMETER	MIN	TYP	MAX	UNIT	COMMENTS
Vee	-3.1	-3.3	-3.5	V	
Ground		0		V	
Vcc	3.1	3.3	3.5	V	
Ivcc		18		mA	
Ignd		42		mA	
Power		260		mW	
Junction Temp.	-40	25	125	°C	
Inputs					
Data rate	0.0		32.0	Gbps	
CM Level	Half of the SE swing below ground power supply				
SE Swing	50	300		mV	Peak-to-peak
Outputs					
CM Level	Half of the SE swing below positive power supply vcc				
SE Swing	360	440		mV	Peak-to-peak
Jitter		< 1		ps	Peak-to-peak

PACKAGE INFORMATION

The chip is packaged into the ADSANTEC's custom 24-pin metal-ceramic package (CQFP). The package mechanical information is available on the company's website.

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REVISION HISTORY

Revision	Date	Changes
1.0	11-2011	Initial Release
1.2	1/2012	Updated description Updated Electrical characteristics Table